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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/699,476	10/31/2003	Hiroyuki Yatsu	9281-4703	2714
7590 06/13/2005			EXAMINER	
Brinks Hofer Gilson & Lione			NORRIS, JEREMY C	
P.O. Box 10395 Chicago, IL 60610			ART UNIT	PAPER NUMBER
3 ,			2841	
			DATE MAILED: 06/13/2005	

Please find below and/or attached an Office communication concerning this application or proceeding.

		Application No.	Applicant(s)			
Office Action Summary			Applicant(s)			
		10/699,476	HIROYUKI YATSU			
	Office Action Summary	Examiner	Art Unit			
	The MAN INO DATE of this committee in	Jeremy C. Norris	2841			
Period fo	The MAILING DATE of this communication app or Reply	ears on the cover sheet with t	ne correspondence address			
THE - Exte after - If the - If NC - Failt Any	ORTENED STATUTORY PERIOD FOR REPLY MAILING DATE OF THIS COMMUNICATION. nsions of time may be available under the provisions of 37 CFR 1.13 SIX (6) MONTHS from the mailing date of this communication. Propriod for reply specified above is less than thirty (30) days, a reply of period for reply is specified above, the maximum statutory period we are to reply within the set or extended period for reply will, by statute, reply received by the Office later than three months after the mailing ed patent term adjustment. See 37 CFR 1.704(b).	within the statutory minimum of thirty (30 ill apply and will expire SIX (6) MONTHS cause the application to become ABANE	be timely filed O) days will be considered timely. From the mailing date of this communication. DONED (35 U.S.C. § 133).			
Status						
1)⊠	Responsive to communication(s) filed on <u>04 Ag</u>	<u>oril 2005</u> .				
2a)⊠ This action is FINAL . 2b)□ This action is non-final.						
3)□	Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under <i>Ex parte Quayle</i> , 1935 C.D. 11, 453 O.G. 213.					
Disposit	ion of Claims					
5)□ 6)⊠ 7)□	Claim(s) 1 and 4 is/are pending in the application 4a) Of the above claim(s) is/are withdraw Claim(s) is/are allowed. Claim(s) 1 and 4 is/are rejected. Claim(s) is/are objected to. Claim(s) are subject to restriction and/or	n from consideration.				
Applicat	ion Papers					
10)⊠	The specification is objected to by the Examine The drawing(s) filed on <u>04 April 2005</u> is/are: a) Applicant may not request that any objection to the Capplacement drawing sheet(s) including the correction of the oath or declaration is objected to by the Ex	☑ accepted or b)☐ objected frawing(s) be held in abeyance. on is required if the drawing(s) i	See 37 CFR 1.85(a). s objected to. See 37 CFR 1.121(d).			
Priority ι	ınder 35 U.S.C. § 119					
12)⊠ a)l	Acknowledgment is made of a claim for foreign All b) Some * c) None of: 1. Certified copies of the priority documents 2. Certified copies of the priority documents 3. Copies of the certified copies of the prior application from the International Bureau See the attached detailed Office action for a list of	have been received. have been received in Applity documents have been received in CPCT Rule 17.2(a)).	ication No ceived in this National Stage			
Attachmen	t(s)					
1) Notice of References Cited (PTO-892) 4) Interview Summary (PTO-413)						
3) 🔲 Inforr	e of Draftsperson's Patent Drawing Review (PTO-948) nation Disclosure Statement(s) (PTO-1449 or PTO/SB/08) r No(s)/Mail Date		ail Date mal Patent Application (PTO-152)			

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DETAILED ACTION

Drawings

The drawings were received on 4 April 2005. These drawings are acceptable.

Claim Rejections - 35 USC § 103

The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.

The factual inquiries set forth in *Graham* v. *John Deere Co.*, 383 U.S. 1, 148 USPQ 459 (1966), that are applied for establishing a background for determining obviousness under 35 U.S.C. 103(a) are summarized as follows:

- 1. Determining the scope and contents of the prior art.
- 2. Ascertaining the differences between the prior art and the claims at issue.
- 3. Resolving the level of ordinary skill in the pertinent art.
- 4. Considering objective evidence present in the application indicating obviousness or nonobviousness.

Claims 1 and 4 are rejected under 35 U.S.C. 103(a) as being unpatentable over US 6,075,711 (Brown) in view of US 6,410,861 B1 (Huang).

Brown discloses, referring to figures 1-3, an electronic unit comprising: a module in which a semiconductor device (10) is attached on a bottom of a circuit board (12) and which has lands (21) provided on the bottom of the circuit board with the lands being connected to the semiconductor device; and a printed circuit board (14) on which the module is mounted, the printed circuit board having conductors having a hole (16) in a position facing the semiconductor device and having electrical conductors (24) to which

the lands are soldered (fig. 2C), wherein solder balls (col. 4, lines 5-25) are applied on the lands and the lands and the electric conductors are soldered together with the solder balls (col. 4, lines 35-45) and the module is mounted on the printed circuit board by soldering the lands to the electrical conductors while the semiconductor device is disposed in the hole (fig 2C). Brown does not specifically state that the wall of the hole has recesses that receive the corresponding solder balls and have electrical conductors applied to the walls of the recesses [claim 1]. Instead, Brown discloses that the solder bumps (22, see col. 4, lines 5-20) are connected along the periphery of the hole without recesses (see fig. 2C). Huang teaches providing conductive recesses (30) along the periphery of an IC package mounting area for receiving solder balls (see fig. 2) to reduce the overall height of the device (see figs. 1 & 2). Therefore, it would have been obvious, to one having ordinary skill in the art, at the time of invention, to use conductive recesses along the periphery of the mounting area of the IC package in the invention of Brown as taught by Huang. The motivation for doing so would have been to reduce the profile of the device thus making it smaller and more versatile to different applications. Moreover, it would have been obvious to specifically place the recesses in the walls of the hole of the invention of Brown so as to minimize the useable board area taken up by the recesses. The motivation being to allow as much space as possible for signal routing. Moreover, it has been held that more than a mere change of form is necessary for patentability. Span-Deck, Inc v. Fab-Con, Inc. (CA 8, 1982) 215 USPQ 835.

Additionally, the modified invention of Brown teaches, wherein the printed circuit board includes a wiring pattern disposed between laminated layers, the wiring pattern

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connected to the electrical conductors (see Brown col. 3, line 30 – col. 4, line 5) [claim 4].

Response to Arguments

Applicant's arguments filed 4 April 2005 have been fully considered but they are not persuasive. Applicant alleges, "Huang specifically describes the structure 30 as a mating support area and differentiates it from the mating electrical interconnect areas 31 (Huang, col. 2, line 52-54). Thus, the structure relied upon is a mechanical structure. whereas the electrical connections are provided by other structures". While it is true that the structure 30 has mechanical properties, Huang specifically states that the mounting structures 36, which are joined to support areas 30, may comprise active structures (col. 3, lines 25-30). The ordinarily skilled artisan would immediately recognize that active mounting structures 36 necessitate electrical connection to support area 30, thus providing electrical as well as mechanical connections. Applicant additionally alleges, "the modification of the teachings of Brown to incorporate the teachings of Huang would result in an inoperative device", citing the difference between the reflow temperatures of the solder disclosed by Huang for support with respect to the solder disclosed by Huang for electrical connection. Stating that due to this difference, if the technique taught by Huang was used to modify Brown, "Prior to the melting of the mechanical connection solder balls 30 (in actuality the connecting balls are 36), the electrical connection solder balls 21 in Brown would be melted and the IC may shift or separate from the circuit board". However, in making such an allegation, Applicant has only considered the teaching of one specific embodiment of Huang and not the general

teaching of the reference as a whole. Huang specifically states that the solder used for the support structures (36) and the electrical connection structures (37) may comprise the same materials (see Huang col. 3, lines 10-15). Thus the alleged problem caused by the difference in reflow temperatures would not even exist because there would be no difference in reflow temperatures.

Having addressed each of Applicant's arguments, the traversal of the rejection on these grounds is deemed unsuccessful.

Conclusion

THIS ACTION IS MADE FINAL. Applicant is reminded of the extension of time policy as set forth in 37 CFR 1.136(a).

A shortened statutory period for reply to this final action is set to expire THREE MONTHS from the mailing date of this action. In the event a first reply is filed within TWO MONTHS of the mailing date of this final action and the advisory action is not mailed until after the end of the THREE-MONTH shortened statutory period, then the shortened statutory period will expire on the date the advisory action is mailed, and any extension fee pursuant to 37 CFR 1.136(a) will be calculated from the mailing date of the advisory action. In no event, however, will the statutory period for reply expire later than SIX MONTHS from the mailing date of this final action.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Jeremy C. Norris whose telephone number is 571-272-1932. The examiner can normally be reached on Monday - Friday, 9:30 am - 5:30 pm.

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If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Kamand Cuneo can be reached on 571-272-1957. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

JCSN

SUPERVISORY PATENT EXAMINER
TECHNOLOGY CENTER 2800